

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definitions
1	IS&R	L4	1	("6085412").PN.	USPAT; US-PGPUB	2004/03/09 08:43		0
2	BRS	L8	1	(Chhor-Khushraw-S.in. Moresco-Larry-L.in.) and @pd>20030921	USPAT; US-PGPUB; EPO; JPO	2004/03/09 08:57		0
3	BRS	L9	224	(257/433.ccls. 257/679.ccls. 361/737.ccls. 902/25.ccls. 902/26.ccls. 902/29.ccls. 235/492.ccls. 235/441.ccls.) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 08:58		0
4	BRS	L10	17	((smart ic chip memory) near3 (card module) same (resin near5 mold\$3)) same (conduct\$3 connect\$3 terminal\$1 pad\$1 contact\$3) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:10		0
5	BRS	L11	1	(smart ic chip memory) near3 (card module) same (resin near5 mold\$3) same (conduct\$3 connect\$3 terminal\$1 pad\$1 contact\$3) and @pd>20030921	EPO; JPO; DERWENT; IBM_TDB	2004/03/09 10:21		0
6	BRS	L12	9	((smart ic chip memory) near3 (card module) same (resin epoxy)) same (lead adj frame) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:22		0
7	BRS	L13	2	((smart ic chip memory) near3 (card module) same (resin epoxy)) same (lead adj frame) and @pd>20030921	EPO; JPO; DERWENT; IBM_TDB	2004/03/09 10:27		0
8	BRS	L14	4	(smart ic chip memory) near3 (card module) same (plate near5 (thermal\$2 heat) near5 conductive) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:28		0
9	BRS	L15	21	(smart ic chip memory) near3 (card module) same (plate near5 (thermal\$2 heat conductive) near5 (packag\$3 resin cas\$3))	USPAT; US-PGPUB	2004/03/09 10:31		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
10	BRS	L16	8	(smart ic chip memory) near3 (card module) same (memory near5 controller near5 (single same one)) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:44			0
11	BRS	L17	8	(smart ic chip memory) near3 (card module) near5 ((single same one) near5 (chip ic monolithic)) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:47			0
12	BRS	L18	0	(smart ic chip memory) near3 (card module) near5 (single same one) near5 (chip ic monolithic) near5 (control1\$3 process\$3 microprocess\$3 microcontrol1\$3) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:48			0
13	BRS	L19	10	(smart ic chip memory) near3 (card module) near5 (single same one) near5 (chip ic monolithic circuit) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:48			0
14	BRS	L20	10	(smart ic chip memory) near3 (card module) near5 (single same one) near5 (chip ic monolithic circuit) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:52			0
15	BRS	L21	7	(smart memory) near3 (card module) near5 (stack\$3 near5 (chip ic)) and @pd>20030921	USPAT; US-PGPUB	2004/03/09 10:57			0